

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/047,379	HUANG ET AL.	
Examiner		Art Unit		Page 1 of 1
Matthew A. Anderson		1765		

U.S. PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
A	US-5245543	09-1993	Smayling et al.	364/468
B	US-5319564	06-1994	Smayling et al.	364/468
C	US-5719495	02-1998	Moslehi	324/158.1
D	US-5152168	10-1992	Barlocchi et al.	73/104
E	US-2002/0157485 A1	10-2002	Cheong	73/865.9
F	US-			
G	US-			
H	US-			
I	US-			
J	US-			
K	US-			
L	US-			
M	US-			

FOREIGN PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
N					
O					
P					
Q					
R					
S					
T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., "Silicon Processing for the VLSI Era Vol: 1 Process Technology", Lattice Press, Sunset Beach, CA, USA, pp. 143-145, 1986.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.